



Clereco

Cross-Layer Early Reliability Evaluation for the Computing cOntinuum



Early Reliability Evaluation in the Computing Continuum

Stefano Di Carlo, Giorgio Di Natale, Dimitris Gizopoulos

COMPUTIN CONTINUUM





PRESENTERS

- **Revisiting Design Methods for Reliable Critical Embedded Systems**
(Philippe BONNOT – Thales, France)
- **Reliability for Industrial Electronics** (Trond LOEKSTAD – ABB Norway)
- **Reliability in High Performance Computing, peanuts or hot Potato?**
(Ramon Canal – UPC Spain)
- **Combining Early Reliability Evaluation with Functional safety requirements: a tool suite for safety design and verification** (Francesco SFORZA - Yogitech Italy)