







An FP7 EU funded research project launched to investigate methodologies for early reliability evaluation for digital systems in the forthcoming computing Continuum

About the project

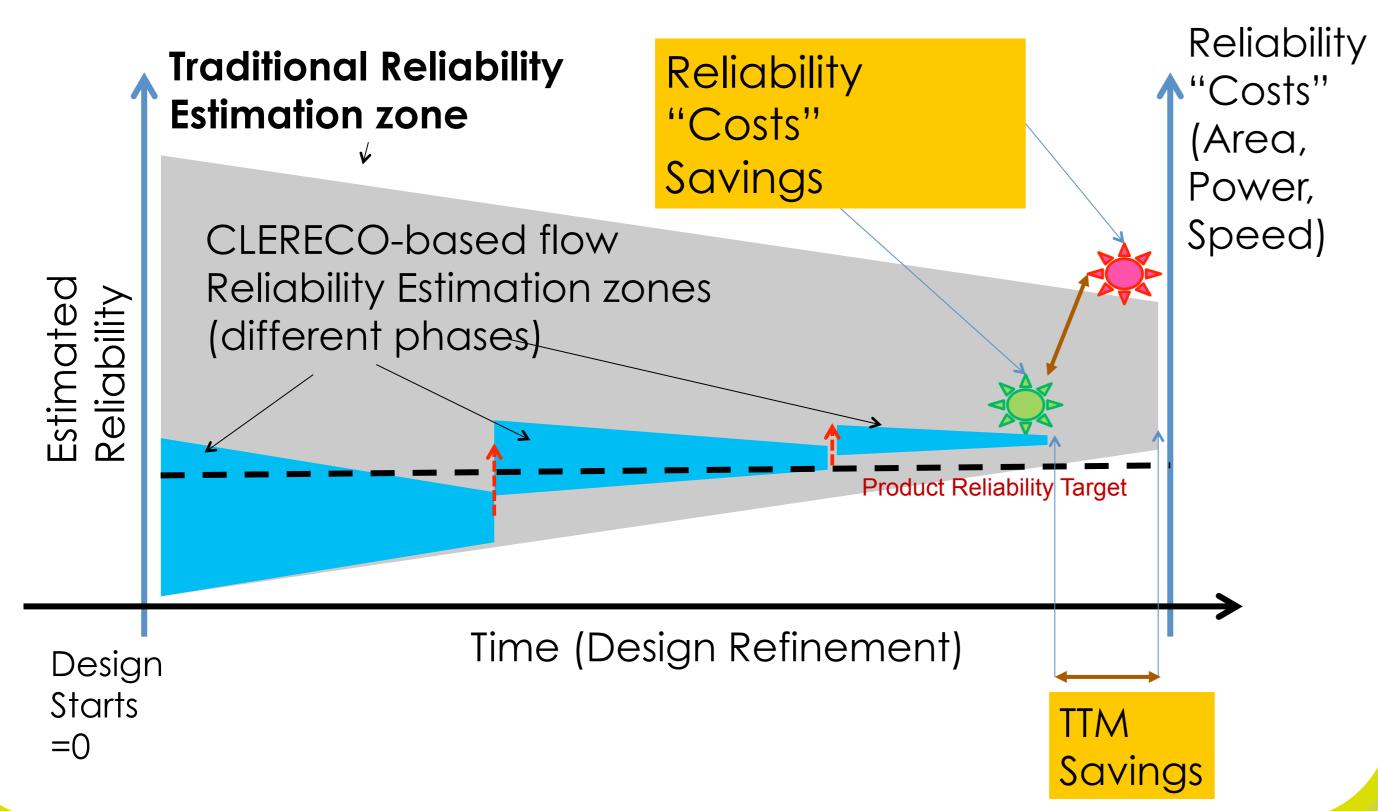
Reliability of electronic systems is a key challenge for the whole information and communication technology and must be guaranteed without penalizing or slowing down the characteristics of the final products.

CLERECO research project recognizes early accurate reliability evaluation as one of the most important and challenging tasks toward this goal.



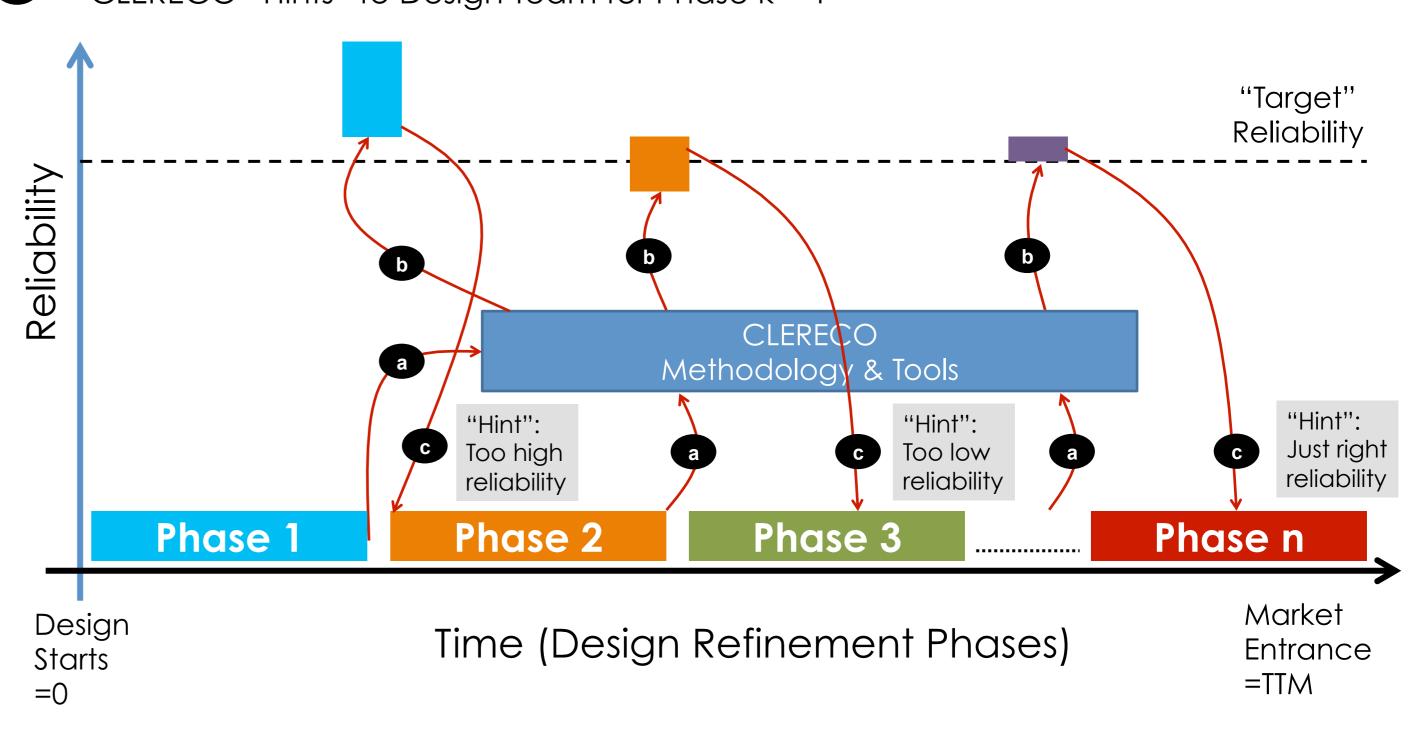
Goals

- ✓ Analyze the reliability of the different hardware and software components at different levels of detail (depending on the design phase).
- Design a flexible, fast, and accurate reliability evaluation framework for the computing continuum
- Comprehensively support the reliability decision-making process in computing systems with a reliability evaluation framework (methodology and tools) that leads to just the right reliability for the system.
- ✓ Validate and evaluate in detail the effectiveness of the CLERECO framework in real-world systems provided by the industrial partners of the consortium.

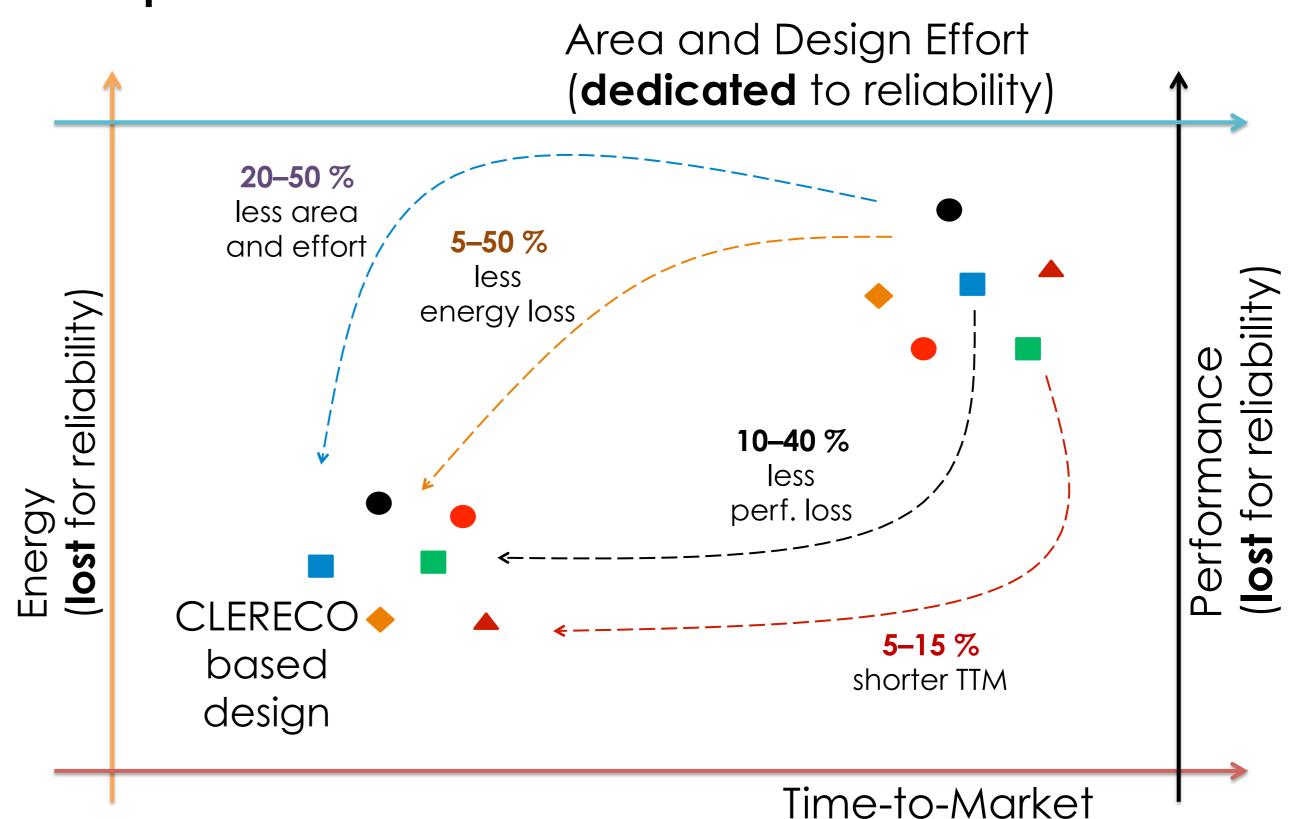


CLERECO design methodology

- a = HW & SW "known" Information Passed to CLERECO Framework from Phase k to Phase k+1
- = CLERECO Framework Reliability Estimation for Phase k
 - = CLERECO "Hints" to Design Team for Phase k + 1



Expected results



Different points correspond to different segments of computing systems market.

Percentage improvements will vary depending on each market's primary optimization points.

Partners



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